

IN/OUT Register program load for the CMC206

This program load allows access to all inputs and outputs as static bits of pulsed (with S1). Individual bits of the ECL I/O are selected for input or output by writing to the direction registers. The Nim I/O does not require a direction bit, since the In and Out are wire-ORed at the connector.

The Camac system (dataway and controller) can be completely tested with the following commands.

F0A6 allows you to read the dataway control lines for the previous command.

F16A7 and F0A7 write and read back a 24 bit register.

The complete list of Camac commands:

F0	A0	Read A inputs
F0	A1	Read B inputs
F0	A2	Read C inputs
F0	A3	Read Nim inputs
F0	A4	Read memory address register
F0	A5	Read lower 24 bits of 32 bit memory
F0	A6	Read ZCFFFFFFAAA from last command (latched on S2)
F0	A7	Read 24 bit test register
F0	A8	Read upper 24 bits of 32 bit memory
F0	A9	Read firmware version number
F1	A0	Read A direction register
F1	A1	Read B direction register
F1	A2	Read C direction register
F10	A0	Clear LAM
F16	A0	Write to A Outputs, 0-16
F16	A1	Write to B Outputs, 0-16
F16	A2	Write to C Outputs, 0-12
F16	A3	Write to Nim Outputs, 0-6
F16	A4	Write to memory address register
F16	A5	Write to lower 24 bits of 32 bit memory
F16	A7	Write to test register, 24 bits
F16	A8	Write to upper 24 bits of 32 bit memory
F17	A0	Write to A direction register, 0= in, 1= out
F17	A1	Write to B direction register, 0= in, 1= out
F17	A2	Write to C direction register, 0= in, 1= out
F18	A0	Pulse A outputs (S1)
F18	A1	Pulse B outputs (S1)
F18	A2	Pulse C outputs (S1)
F18	A3	Pulse N outputs (S1)
F25	A0	Set Lam
F27	A0	Test Lam